LIST OF PUBLICATIONS

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- 1) Revathi.S, Chaitra.C, (2017) Blu-Ray Media Encryption and Decryption on MAGIK Board based on I.MX6 Processor, Grenze International Journal of Computer Theory & Engineering, Special issue, Grenze ID: 01.GIJCTE.3.4.7.1, Grenze Scientific Society, 2017
- 2) Revathi.S, Chaitra.C, (2017) A Survey on Blu-ray Content Protection mechanism, International Journal of Engineering, Basic Sciences, Management and Social studies, Vol 1, Issue 1, pp:435-437.

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3) Chaitra C (2017) "Nine Level Reduced Switch Multilevel DC Link Inverter Topology using SPWM Technique," Journal of Instrument Society of India, ISSN 0970-9983, Vol.47, No.1, March-2017, pp-15-19.

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- **4) Chaitra C** and K.C Rupesh, (**2015**) "Simulation of Three-Phase Multilevel Inverters for Two Topologies Using MATLAB," *Emerging Trends in Engineering Technologies (ETET-2015)*, 20 & 20 Feb 2015, Jyothy Institute of Technology, Bengaluru.
- **5)** Chaitra C and K.C Rupesh, (2015) "Multilevel Inverter Topology with Reduced Number of Switches," *Research Challenges in Power, Control, Communication and Instrumentation Leading to Sustainable Technologies (NCPCCI-2015)*, 24 & 25 April 2015, R.V. College of Engineering, Bengaluru.
- **6)** Revathi.S, **Chaitra.C**, **(2017)** A Survey on Blu-ray Content Protection mechanism, National Conference on Emerging Trends in Electronics and Communication (NCETEC-92), May 18th and 19th 2017, BGS Institute of Technology, Mandya, published in International Journal of Engineering, Basic Sciences, Management and Social studies, Vol 1, Issue 1, pp 435-437.
- **7) Chaitra C** (2015) "Nine Level Reduced Switch Multilevel DC Link Inverter Topology using SPWM Technique," National symposium of Instrumentation-40, Kumaraguru College of Engineering, Coimbatore, October 30th, 31st 2015.

- **8)** Chaitra C and K.C Rupesh, (2015) "Three-Phase Multilevel Inverter Topology with Reduced Number of Switches," *International Conference on Recent Advances in Science and Engineering (ICRASE-2015)*, 9 & 10 May 2015, Bengaluru.
- **9**) Revathi.S, **Chaitra.C**, **(2017)** Blu-Ray Media Encryption and Decryption on MAGIK Board based on I.MX6 Processor, International Conference on Signal, Image Processing Communication and Automation ICSIPCA- 2017, July 6th and 7th 2017, JSS Academy of Technical Education, Bengaluru, pp 312-316.